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**FROM: Winston Hsu, PATENT AGENT, REG. NO. : 41,526**

**SERIAL NO.: 10/064,036**

**ATTORNEY DOCKET NO.: NAUP0500USA**

**SUBJECT: SUBMISSION OF IDS PREVIOUSLY LISTED  
IN NOVEMBER 13 , 2003**

**TOTAL PAGES: 58 PAGES (INCLUDING COVER PAGE)**

**Winston Hsu 2004/04/01**

**NAUP0500USA**

PTO/SB/088 (10-01)

Approved for use through 10/31/2002. OMB 0851-0031

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	10/064.036
		Filing Date	06/04/2002
		First Named Inventor	Steve S. Chung
		Group Art Unit	2829
		Examiner Name	Pert, Evan T
		Attorney Docket Number	NAUP0500USA
Sheet	1	of	1

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
	1	LEWIS M. TERMAN, SOLID-STATE ELECTRONICS, Vol. 525, 1962, p. 285-299	
	2	P. HEREMANS ET AL., IEEE T-Ed, Vol. 36, 1989, p. 1318-1335	
	3	S.S. CHUNG ET AL., Proc. SSDM, 1993, p. 841-843	
	4	C. Chen ET AL., IEEE T-Ed, Vol. 45, No. 2, 1998, p. 512-519	
	5	S.S. CHUNG ET AL., IEEE T-Ed, Vol. 46, No. 7, 1999, p. 1371-1377	
	6	P. MASSON ET AL., IEEE Ed1, Vol. 20, No. 2, 1999, p. 92-94	

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